Inventor: Marumoto

Invenior: Marumoto
Docket No.: 12844.40US01
Title: IMAGE DISPLAY SYSTEM AND DISPLAY DEVICE
Attorney Name: Douglas P. Mueller
Phone No.: (612) 371-5237

Sheet 1 of 4

20 21 22 23 24 26 26

Camera Image processing IC

Camera I/F circuit

Area memory Transfer address generating circuit Buffer memory

Work memory Display panel Buffer memory control circuit Address converting circuit

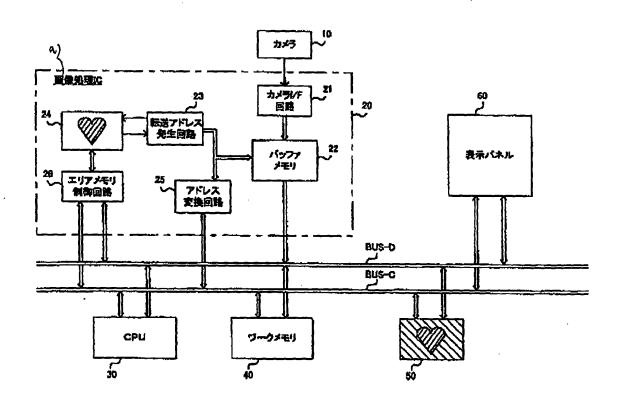


Fig.

Inventor: Marumoto Docket No.: 12844.40US01

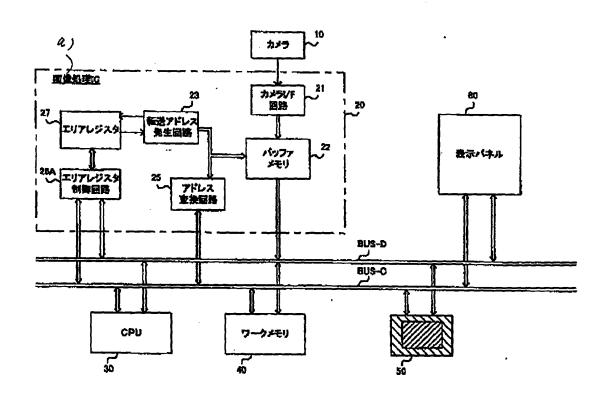
Title: IMAGE DISPLAY SYSTEM AND DISPLAY DEVICE Attorney Name: Douglas P. Mueller

Phone No.: (612) 371-5237

a) 10 21 22 23 23 24 25 26 26 26 40 60 Display panel Work memory Area register Buffer memory control circuit Buffer memory Camera I/F circuit Area register control circuit Address converting circuit Area memory Transfer address generating circuit Image processing IC

Fig.

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a)

エリアメモリ 対策回路

CPU

直接基準(

a) 100 211 222 23 24 25 26 28 29

Fig.

W

Image processing IC Camera

Area memory Buffer memory Camera I/F circuit

Buffer memory control circuit Address converting circuit

Display panel Work memory Gate circuit

Read-out address generating circuit Transfer address generating circuit カメラ カノランド 回路 徒出アドレス 発生回路 バッファ メモリ 表示パネル アドレス

> BUS-0 BUS-0

ワークメモリ

Title: IMAGE DISPLAY SYSTEM AND DISPLAY DEVICE Attorney Name: Douglas P. Mueller Phone No.: (612) 371-5237 Sheet 4 of 4

a) 10 21 22 23 23 24 25 26 26 26 28 Work memory Display panel Gate circuit Read-out address generating circuit buffer memory control circuit Buffer memory control circuit Area memory Transfer address generating circuit Camera I/F circuit Address converting circuit Buffer memory Image processing IC

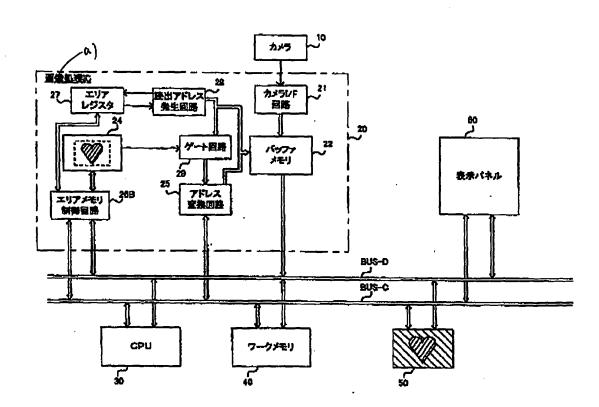


Fig.